IN THE SPECIFICATION

Please rewrite the paragraph beginning on page 16, line 18, and ending on page 17, line 1, as follows:

For example, in a case of a semiconductor integrated circuit having a model name of 6595E00 ~~that~~ it is designed by a circuit designer whose circuit designer ID is AAA, its <u>design</u> technology is ~~COMS9HD,~~ <u>CMOS9HD, a proprietary design rule for CMOS (complementary metal-oxide semiconductor), the source of</u> its ~~condition~~ <u>design</u> is ~~COMS3, 3V,~~ <u>CMOS 3.3V, one of the libraries regarding CMOS9HD in a computer-aided design (CAD) system for designing a semiconductor circuit,</u> and its package is TBG. As the various inspection results of this semiconductor integrated circuit, there is no error in the net list check, there are two errors in the pattern check, the scan check is not executed, and there is no error in the timing check, in the acceptance inspection executed on September 11.

Please rewrite the paragraph beginning on page 20, line 21, and ending on page 21, line 6, as follows:

After that, the layout designer designs the layout (Step S13). After the design of the layout, a back annotation is carried out (Step S14). In the back annotation, it is confirmed whether or not the semiconductor integrated circuit carries out a desirably functional operation at a delay after the layout, and additionally writes its result to the model development history database 7. If the result of the back annotation is NG, it is returned back to the circuit designer, and the improvement based on the re-design is requested. If the result of the back annotation is OK, an ~~EB process~~ <u>Electron Beam data conversion process (EB process)</u> is carried out (Step 15).

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